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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

In re application of Christopher M. Cischke
Title: Method and Apparatus for
Creating Integrated Circuit
Simulator Source Files

Serial No.: 10/783,816
Filed: February 20, 2004
Docket No.: RA 5548 (103.001)
Group No.: 2186

Commissioner for Patents
P O Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED FILING RECEIPT

1. Attached is a copy of the official filing receipt received from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested.

2. There is an error with respect to the following data, which is:

- ☒ incorrectly entered
☐ omitted and/or

- | | | Error in |
|----|-------------------------------------|-----------------------------|
| 1. | <input type="checkbox"/> | Applicant's name |
| 2. | <input type="checkbox"/> | Applicant's address |
| 3. | <input checked="" type="checkbox"/> | Title |
| 4. | <input type="checkbox"/> | Filing Date |
| 5. | <input type="checkbox"/> | Serial Number |
| 6. | <input type="checkbox"/> | Foreign/PCT Application Re: |
| 7. | <input type="checkbox"/> | Other |

- | | Correct Data |
|----|---|
| 1. | |
| 2. | |
| 3. | Method and Apparatus for
Creating Integrated Circuit
Simulator Source Files |
| 4. | |
| 5. | |
| 6. | |
| 7. | |

3. ☒ The correction is not due to any error by applicant and no fee is due.

Respectfully submitted,

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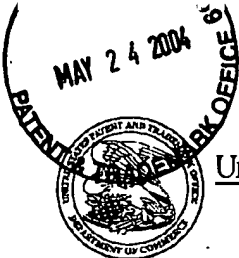
CAJ/eav

Enclosures

I hereby certify that this correspondence is being deposited in the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on (date).

Charles A. Johnson
Attorney for Applicants

Signature
May 20, 2004
Date of Signature

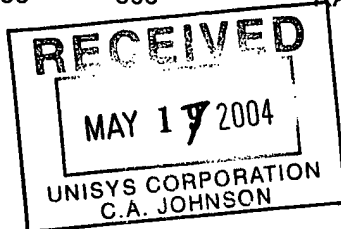


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/783,816	02/20/2004	2186	806	RA 5548 (103.001)	8	22	3

Charles A. Johnson
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CONFIRMATION NO. 7069

FILING RECEIPT



OC000000012665684

Date Mailed: 05/14/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Christopher M. Cischke, Shoreview, MN;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 05/13/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Method and apparatus for creating integrated circuit simulator test source files

Preliminary Class

711

Kardex		
DKT Entry	5/20/04	ean
DKT Folder	↓	↓
Follow-Up	↓	↓
Mgt Review	5/20/04	CAA

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